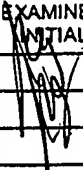
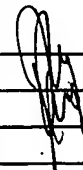

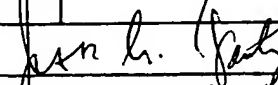


Form PTO 1449 (Modified)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY DOCKET NO. 244398US-2 CONT		SERIAL NO. 10/24825 NEW APPLICATION	
LIST OF REFERENCES CITED BY APPLICANT				APPLICANT Tomoko MATSUDAI, et al.			
				FILING DATE HEREWITH		GROUP	
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	AA	5,466,951	11/14/95	Heinrich BRUNNER, et al.			
	AB	2003/0071305	4-2003	MATSUDAI ET AL.			
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						
	AM						
	AN						
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION YES NO		
	AO	11-274484	10/8/99	JAPAN (w/English Abstract)			X
	AP	6-318706	11/15/94	JAPAN (w/English Abstract)			X
	AQ						
	AR						
	AS						
	AT						
	AU						
	AV						
OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)							
	AW	Tomoko MATSUDAI, et al., "Ultra High Switching Speed 600 V Thin Wafer PT-IGBT Based on New Turn-Off Mechanism", INTERNATIONAL SYMPOSIUM ON POWER SEMICONDUCTOR DEVICES & ICs '02, PP. 285-288					
	AX	Tomoko MATSUDAI, et al., "Advanced 60µm Thin 600V Punch-Through IGBT Concept for Extremely Low Forward Voltage and Low Turn-off Loss", PROCEEDINGS OF 2001 INTERNATIONAL SYMPOSIUM ON POWER SEMICONDUCTOR DEVICES & ICs, OSAKA, pp. 441-444					
	AY	T. LASKA, et al., "The Field Stop IGBT (FS IGBT) - A New Power Device Concept with a Great Improvement Potential", ISPSD'2000, May 22-25, pp. 355-358					
	AZ						
Examiner 					<input type="checkbox"/> Additional References sheet(s) attached		
Date Considered 3/20/05							
*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							